

Help Contact Us Privacy & Security IEEE.org

restaure of inspec

3331

STO

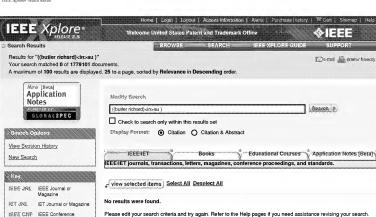
IET CNF

Proceeding

IET Conference Proceeding

IEEE Standard

& Copyrigm (CGG IEBE - SE Boltmi Beceived)



Help Contact Us Privacy & Security IEEE.org

inspec

3331

STO

SET ONE

Proceeding

IET Conference Proceeding

IEEE Standard

& Copyrigm (CGS IEEE - AE Boutts Bacarvaci



Future.', International Volume 3, 27-31 May 1996 Page(s):1408 - 1410 vol.3 AbstractPlus | Full Text: PDF(296 KB) 1888 CNF Rights and Permissions

> Help Contact Us Privacy & Security IEEE.org C Conglom (150 IEEE - A) Allons Reserved

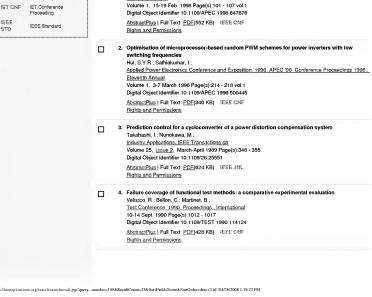
@ inspec

3331

STO

Proceeding

IEEE Standard



П	Schen Kupf America priority queue Kahrs, M.:  Micro, IEEE Volume 13, Issue 4, Aug. 1993 Page(e) 49 - 51  Digital Object Identifier 10 1109/40 229715  AbstractPlus Full Text. PDE(308 KB) IEEE JNL  Bights and Permissions
	6. Speed versus Accuracy Trade-Offs in Microarchitectural Simulations Yi, J.J.; Sendag, R.; Lilja, D.J.; Hawkins, D.M.; Computers, IEEE Transpations on Volume 56, Issue 11, Nov. 2007 Page(s):1549 - 1563 Digital Object Identifier 10.1109/TC.2007.70744 AbstractPlus   Full Text; PDE(f766 KB) IEEE JINL
	Rights and Permissions  7. The SYNARGOS ambient noise measurement buoy Burke, S.; Buck, B.; Luther, C.;
	OCEANS Volume 14, Sep 1982 Page(s):626 - 631 AbstractPlus   Full Text: PDE(692 KB)   IEEE CNF Bights.and.Permissions
	DPA Using Phase-Based Waveform Matching against Random-Delay Countermeasure Nagashima, S.; Homma, N.; Imal, Y.; Aoki, T.; Satoh, A.;  Clicusts and Systems. 2007. ISCAS 2007. IEEE International Symposium on 27-30 May 2007 Page(s):1807 - 1810  Digital Object Identifier 10.1109/ISCAS.2007.378024  AbstatePlay   Full Text. PGE/G57 KB) IEEE CNF
0	Rights and Permissions     Experiences in dynamic placement of actors on multicomputer systems     Di Santo, M.; Fratolisilo, F.; lannello, G.;     Parallel and Distributed Processina, 1995; Proceedings, Euromicro Workshop on
	25-27 Jan. 1995 Page(s):130 - 137 Digital Object Identifier 10.1109/EMPDP.1995.389146 AbstractPlus   Full Text: <u>PDE(</u> 1700 KB)   IEEE CINF Bights and Permissions
	10. Hardware implementation for fast convolution with a PN code using field programmable gate array Alaqueeli, A.; Starzyk, J.; System Theory, 2001 Proceedings of the 33rd Southeastern Symposium on 18-20 March 2001 Page(6):197-201 Digital Object Identifier 10.109/SSST.2001.918517
	AbstractPlus   Full Text. PDF(368 KB)   IEEE CNF Bights and Permissions
	11. Comparison and Impact of Substrate Noise Generated by Clocked and Clockless Digital Circuitry Le, J.; Hanken, C.; Held, M.; Hagedorm, M.; Mayaram, K.; Fiez, T.S.; Conference 2006. IEEE Custom Integrated Circuits 10-13 Sept. 2006 Page(5):105-108 Digital Object Identifier 10:1109/CICC 2006.321003
	AbstractFlus   Full Text PDF(269 KB)   LEEE CNF Bights and Permissions





Minspec

Miczapyriam 1908 IEEE - All Arany Reserved